INFO: [VRFC 10-2263] Analyzing Verilog file "T:/mux2to1.v/mux2to1.v.srcs/sources\_1/new/mux2to1.v" into library xil\_defaultlib

INFO: [VRFC 10-311] analyzing module mux2to1

INFO: [VRFC 10-2263] Analyzing Verilog file "T:/mux2to1.v/mux2to1.v.srcs/sim\_1/new/mux2to1\_tb.v" into library xil\_defaultlib

INFO: [VRFC 10-311] analyzing module mux2to1\_tb

INFO: [VRFC 10-2263] Analyzing Verilog file "T:/mux2to1.v/mux2to1.v.sim/sim\_1/behav/glbl.v" into library xil\_defaultlib

INFO: [VRFC 10-311] analyzing module glbl